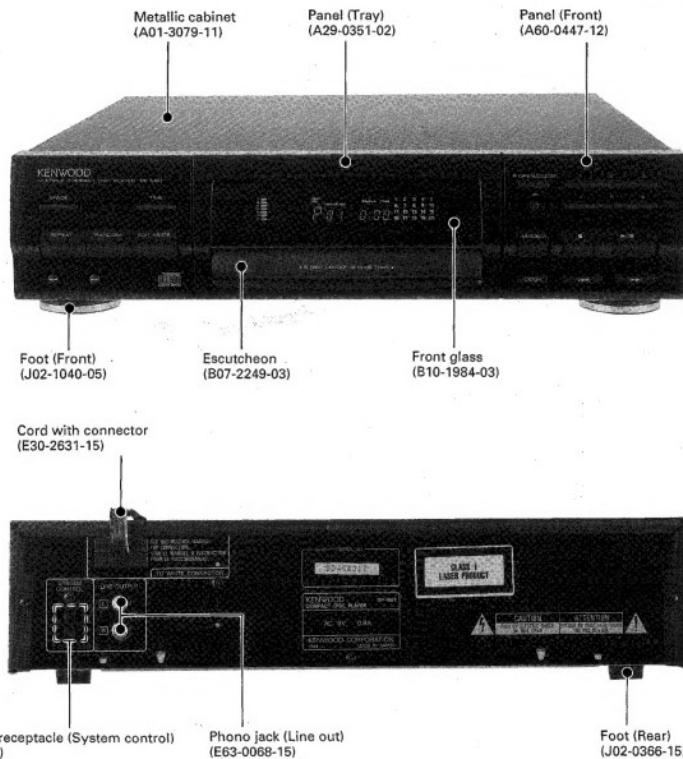


COMPACT DISC PLAYER
DP-M87
SERVICE MANUAL

ADDITIONAL

KENWOOD

© 1994-2 PRINTED IN JAPAN
B51-4867-00(0)626



This service manual is available of changing information from serial No. 31240001.

Refer to DP-MA5/MA9 service manual (B51-4588-00), if need description in detail.

CAUTION : When doing repair of DP-M87 be sure to have the customer bring the A-57, A-77, A-87, A-97 or use power supply jig RM-90PS, or supply to 9V AC to terminal Nos 1 and 2 of WH4 on the X25-5350 (X25-5440) PC board ass'y. If not get 9V AC, please order the A-848's power transformer (parts No. L07-0038-05 / 120V / 220V / 240V). Refer to the DP-911 service manual. Don't use the "RHEOSTAT".

In compliance with Federal Regulations, following are reproductions of labels on, or inside the product relating to laser product safety.

KENWOOD Corp. certifies this equipment conforms to DHHS Regulations No. 21 CFR 1040.10, Chapter 1, Subchapter J.

**DANGER : Laser radiation when open and interlock defeated.
AVOID DIRECT EXPOSURE TO BEAM.**

DP-M87

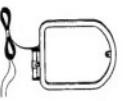
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NOTE : Refer to DP-MA5/MA9 service manual (B51-4588-00), if need description in detail.

ACCESSORIES Magazine is packed with the CD player.

• FM indoor antenna	1	• AM loop antenna	1	• Antenna adaptor	1	• Loop antenna stand	1
(T90-0175-05)		(T90-0174-05)		(T90-0185-05) : 75Ω / 300Ω T.E type only		(J19-2815-04)	
							
• Audio cords (E30-0505-05)	3	• System control cords (E30-2627-05)	1	• AC plug adaptor	1	• Magazine	1
(E30-0615-05)	1	(E30-2628-05)	1	(E03-0115-05) : M type only		(J19-3394-13)	
							
• Battery (AAA/R03)	2	• Remote control unit	1				
(-)		(X94-1011-41 : RC-77M) K-77M / 88M, MIDI M-57M / M-77M (X94-1050-11 : RC-97M) K-99M, MIDI M-97M					
							

Battery cover (A09-0126-03) K-77M / 88M, MIDI M-57M / M-77M
Battery cover (F07-0721-23) K-99M, MIDI M-97M

For M,X type

System name	Amp	Tuner	Cassette deck	CD player	Graphic equalizer (option)	Speaker
K-77M	A-57	T-76	X-57	DP-M87	GE-560	S-6M
K-88M	A-77	T-76	X-87	DP-M87	GE-760	S-8M
K-99M	A-87	T-76	X-87	DP-M87	GE-970	S-10M

For E,T type

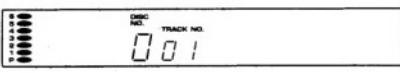
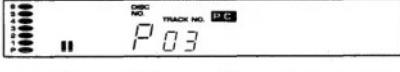
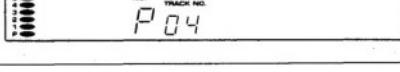
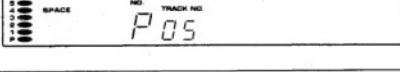
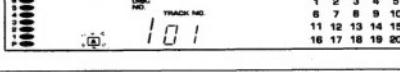
System name	Amp	Tuner	Cassette deck	CD player	Graphic equalizer (option)	Speaker
MIDI M-57M	A-57	T-76L	X-57	DP-M87	GE-560	LS-56
MIDI M-77M	A-77	T-76L	X-87	DP-M87	GE-760	LS-76
MIDI M-97M	A-97	T-76L	X-87	DP-M87	GE-970	LS-97

CIRCUIT DESCRIPTION

1. Test Mode

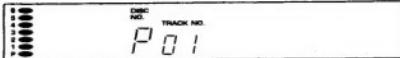
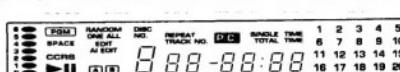
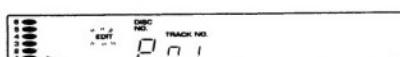
Setting the test mode

This microprocessor built in this unit (X32-) can be put to TEST MODE by just short-circuiting the test pins (#2 and #3).

No.	Input key	Function	Display																				
1	STOP	(1) Focusing servo OFF (2) Tracking servo OFF (3) Feed servo OFF																					
2	REPEAT	(1) Laser (In STOP mode only) ON																					
3	RANDOM	(1) Focusing servo ON (2) Tracking servo OFF (3) Feed servo OFF																					
4	TIME	(1) Focusing servo ON (2) Tracking servo ON (3) Feed servo OFF																					
5	PLAY	(1) Focusing servo ON (2) Tracking servo ON (3) Feed servo ON																					
6	DISC 1	Load No.1 disc to No.6 in order.	 <table border="1"><tr><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td></tr><tr><td>6</td><td>7</td><td>8</td><td>9</td><td>10</td></tr><tr><td>11</td><td>12</td><td>13</td><td>14</td><td>15</td></tr><tr><td>16</td><td>17</td><td>18</td><td>19</td><td>20</td></tr></table>	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
1	2	3	4	5																			
6	7	8	9	10																			
11	12	13	14	15																			
16	17	18	19	20																			
7	DISC 2	Read the TOC (table of contents) of disc No.3 to No.6 in order. TEST mode is cancelled after reading the TOC of No.6 disc, and then playback the 1st track.	 <table border="1"><tr><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td></tr><tr><td>6</td><td>7</td><td>8</td><td>9</td><td>10</td></tr><tr><td>11</td><td>12</td><td>13</td><td>14</td><td>15</td></tr><tr><td>16</td><td>17</td><td>18</td><td>19</td><td>20</td></tr></table>	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
1	2	3	4	5																			
6	7	8	9	10																			
11	12	13	14	15																			
16	17	18	19	20																			

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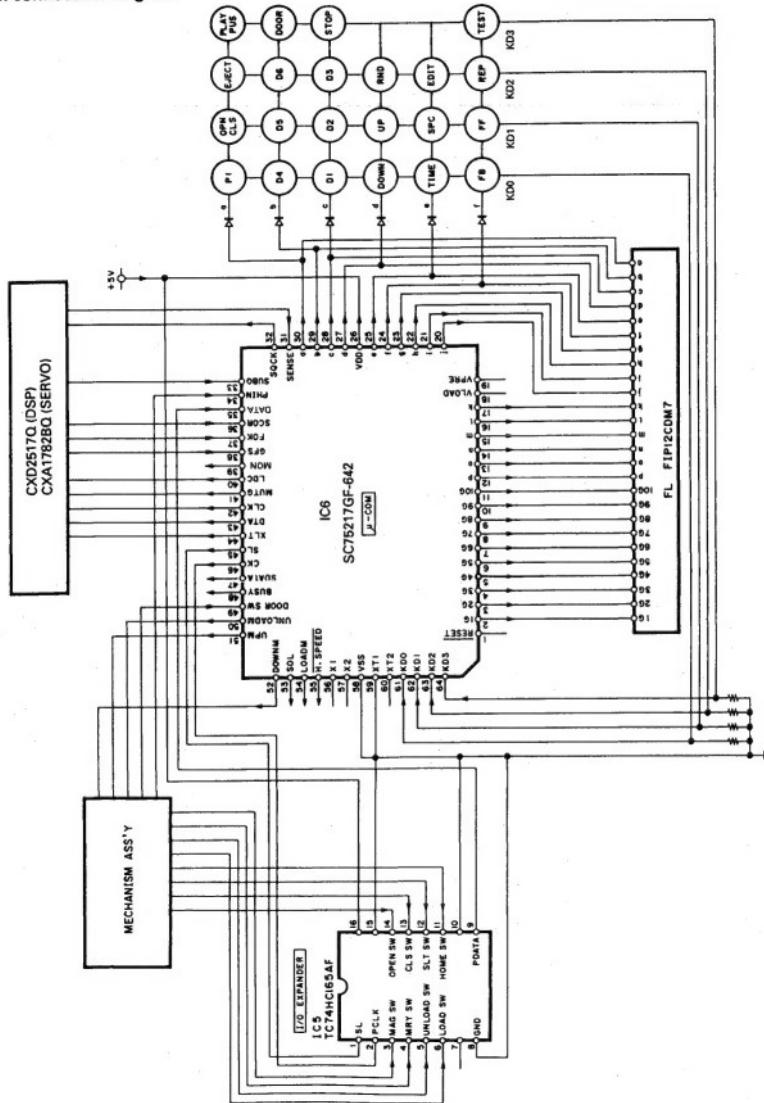
CIRCUIT DESCRIPTION

No.	Input key	Function	Display
8	DISC 3 ~ 6 DISC P	Load the decided No. disc which is pressed by the key and set to STOP mode. ex. Disc No.4 key is pressed (PLAY, CHECK and CLEAR keys are available to operate).	
9	UP	Turns all FL display lamps ON.	
10	DOWN	Turns all FL display lamps OFF. "DISC" and "1 ~ 6" are not off because circuit is static operation.	
11	EDIT	(1) Door opens. (2) P1 tray come out. Press "EDIT" key, "PLAY MODE".	
12	FF	In the STOP mode, moves the pickup slightly toward the outer position of disc.	
13	FB	In the STOP mode, moves the pickup slightly toward the inner position of disc.	
14	SPACE	High-speed playback CHECK mode (in stop mode only) playback P1 disc in high-speed mode. If press "SPACE" key, change to normal mode. In this mode, all keys are available.	

CIRCUIT DESCRIPTION

2. Microprocessor : SC75217GF-642 (IC6)

2-1. Pin connection diagram



CIRCUIT DESCRIPTION

2-2. Pin functions : SC75217GF-642 (IC6)

Pin No.	Pin name	I/O	Function
1	RESET	-	Reset input port
2 ~ 11	1G ~ 10G	O	FL grid control port
12 ~ 17	p ~ k	O	Not used
18	VLOAD	I	FL driver negative power supply
19	VPRE	I	FL pre-driver power supply
20 ~ 25	j ~ e	O	FL grid control port also used for key-scan
26	VDD	-	+5V power supply
27 ~ 30	d ~ a	O	FL grid control port also used for key-scan
31	SENSE	I	Signal detection port for SENSE signal from signal processor and servo IC
32	SQCK	O	Q-data read clock output port
33	SUBQ	O	Q-data input port
34	PHIN	I	Photo interrupter input port for mechanism (PH1)
35	DATA	I	Data input from TC74HC165AF
36	SCOR	I	Sub-code frame sync detection signal input port
37	FOK	I	Input port of FOK signal from RF amp
38	GFS	I	Input port of frame sync signal
39	MON	O	ON/OFF control output of disc motor
40	LDC	O	Laser ON/OFF signal output
41	MUTG	O	Mute port of signal processor
42	CLK	O	Signal processor and servo IC control out port (CLOCK)
43	DATA	O	Signal processor and servo IC control out port (DATA)
44	XLT	I	Signal processor and servo IC control out port (LATCH)
45	S/L	I	Latch output port of TC74HC165AF
46	CK	O	Clock output port of TC74HC165AF
47	SDATA	I/O	Serial DATA in/out port
48	BUSY	I/O	Serial BUSY in/out port
49	DOORSW	O	Door switch input port of mechanism
50	UNLOADM	O	Control port of unloading motor for mechanism
51	UPM	O	Control port of up motor for mechanism
52	DOWNM	O	Control port of down motor for mechanism
53	SOL	O	Control port of solenoid for mechanism
54	LOADM	O	Control port of loading motor for mechanism (LM)
55	H.SPEED	O	High-speed control port (Active L)
56	X1	I	Oscillation input port (4.19MHz)
57	X2	-	NC
58	Vss	-	GND
59	XT1	-	GND
60	XT2	-	NC (Open)
61 ~ 64	KD0 ~ 3	I	Key input port

2-3. Pin functions : TC74HC165AP (IC3)

Pin No.	Pin name	I/O	Function
1	SL	I	Shift load input
2	PCLK	I	Clock input
3	MAGSW	I	Magazine switch (SW4)
4	MRYSW	I	Memory switch (SW3)
5	UNLOADSW	I	Unload switch (SW5)
6	LOADSW	I	Load switch (SW6)
7	-	O	No use
8	GND	-	Ground
9	PDATA	O	Data output
10	-	I	No use
11	HOMESW	I	Home position switch (SW2)
12	SLTSW	I	Start limit switch (SW1)
13	CLSSW	I	Tray close switch (SW6)
14	OPNSW	I	Tray open switch (SW6)
15	-	I	No use
16	Vcc	-	Power supply (+5V)

2-4. TOC data output of serial codes for AI file

When the CD player reads the TOC data of a disc (in the play mode), the following serial codes (16 bits) are output.

• CD MAX TRACK No. [61XX]

Model code 61H
Function code XXH (Max TNO)

• CD TOTAL TIME (min.) [62XX]

Model code 62H
Function code XXH (Total time in min.)

• CD TOTAL TIME (sec.) [62XX]

Model code 63H
Function code XXH (Total time in sec.)

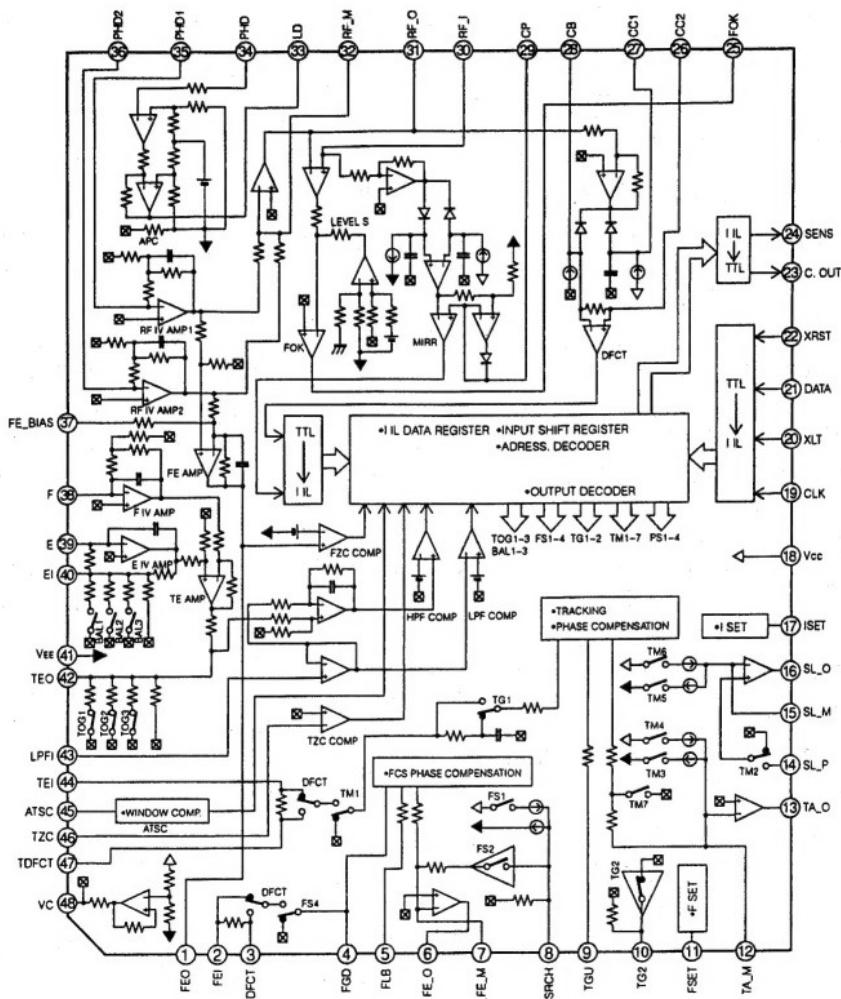
Example

When a disc containing 20 tunes of 65 minutes and 2 seconds in total is played, the following three codes [6120], [6265] and [6302] are output continuously.

CIRCUIT DESCRIPTION

3. Servo IC : CXA1782BQ (IC1)

3-1. Block diagram



CIRCUIT DESCRIPTION

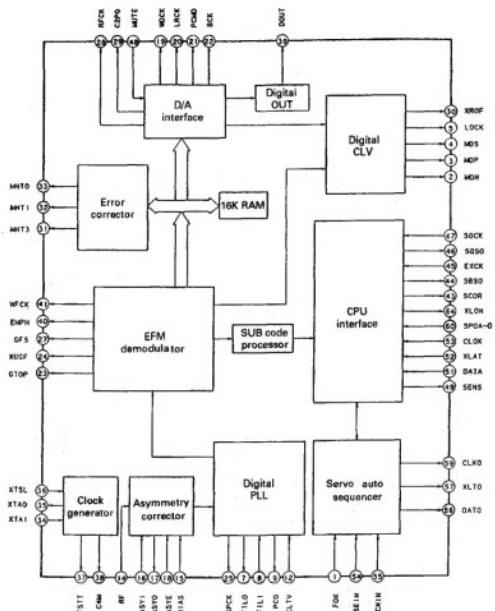
3-2. Pin function

No.	Pin name	I/O	Function
1	FEO	I	Focus error amplifier output. Connected internally to FZC comparator input.
2	FEI	I	Focus error input.
3	FDFCT	I	Capacitor connection pin for detect time constant.
4	FGD	I	Ground this pin through a capacitor when decreasing the focus servo high-frequency gain.
5	FLB	I	External time constant setting pin for increasing the focus servo low-frequency.
6	FE-O	O	Focus drive output.
7	FL-M	I	Focus amplifier negative input pin.
8	SRCH	I	External time constant setting pin for generating focus servo waveform.
9, 10	TGU, TG2	I	External time constant setting pin for switching tracking high-frequency gain.
11	TSET	I	High cut off frequency setting pin for focus and tracking phase compensation amplifier.
12	TA-M	I	Tracking amplifier negative input pin.
13	TA-O	O	Tracking drive output.
14	SL-P	I	Feed amplifier non-inversed input.
15	SL-M	I	Feed amplifier negative input pin.
16	SL-O	O	Feed drive output.
17	ISET	I	Setting pin for focus search, track jump, and feed kick current.
18	Vcc	-	
19	CLK	I	Serial data transfer clock input from CPU (no pull up resistance).
20	XLT	I	Latch input from CPU (no pull up resistance).
21	DATA	I	Serial data input from CPU (no pull up resistance).
22	XRST	I	Reset input; resets at low (no pull up resistance).
23	C.OUT	O	Track number count signal output.
24	SENS	O	Outputs FZC, DFCT, TZC gain, balance and others according to the command from CPU.
25	FOK	O	Focus OK comparator output (DC voltage : 10kΩ load resistance is connected).
26	CC2	O	Input pin for the DEFECT bottom hold output capacitance-coupled.
27	CC1	I	DEFECT bottom hold output.
28	CB	I	Connection pin for DEFECT bottom hold capacitor.
29	CP	I	Connection pin for MIRR hold capacitor. MIRR comparator non-inversed input.
30	RF-I	I	Input pin for the RF summing amplifier output capacitance-coupled.
31	RF-O	O	RF summing amplifier output. Eye pattern check point.
32	RF-M	I	RF summing amplifier inversed input. The RF amplifier gain is determined by the resistance connected between this pin and RFO pin.
33	LD	O	APC amplifier output.
34	PHD	I	APC amplifier input.
35, 36	PHD1, PHD2	I	RF I-V amplifier inversed input. Connect these pins to the photo diode A+C and B+D pins.
37	FE-BIAS	I	Bias adjustment of focus error amplifier.
38, 39	F, E	I	F-I-V and E-I-V amplifier inversed input. Connect these pins to photo diodes F and E.
40	EI	-	I-V amplifier E gain adjustment (when not using automatic balance adjustment).
41	VEE	-	
42	TEO	O	Tracking error amplifier output. E-F signal is output.
43	LPFI	I	Comparator input for balance adjustment (input from TEO through L.P.F.).
44	TEI	I	Tracking error input.
45	ATSC	I	Window comparator input for ATSC detection.
46	TZC	I	Tracking zero-cross comparator input.
47	TDFCT	I	Capacitor connection pin for defect time constant.
48	VC	O	(Vcc+VEE) / 2 DC voltage output.

CIRCUIT DESCRIPTION

4. Digital Signal Processor : CXD2517Q (IC2)

4-1. Block diagram



4-2. Pin function

No.	Pin name	I/O	Function
1	FOK	I	Focus OK input. Used for SENS output and the servo auto sequencer.
2	MON	I, O	Disc motor ON/OFF control output.
3, 4	MDP, MDS	O, I, Z, O	Disc motor servo clock.
5	LOCK	O, I, O	GFS is sampled at 460Hz; when GFS is high, this pin outputs a high signal. If GFS is low eight consecutive samples, this pin outputs low.
6	TEST	I	Test pin (normally GND).
7	FILO	O, Analog	Master PLL (slave=digital PLL) filter output.
8	FILI	I	Master PLL filter input.
9	PCO	O, I, Z, O	Master PLL charge pump output.
10	Vss	-	GND
11	AVss	-	GND (analog)
12	CLTV	I	Master VCO control voltage input.
13	AVdd	-	Analog power supply (+5V).
14	RF	I	EFM signal input.
15	BIAS	I	Constant current input of asymmetry circuit.

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CIRCUIT DESCRIPTION

No.	Pin name	I/O	Function
16	ASYI	I	Asymmetry comparator voltage input.
17	ASYO	O 1, 0	EFM full-swing output (low=Vss, high=Vdd).
18	ASYE	I	Low : asymmetry circuit off, high : asymmetry circuit on.
19	WDCK	O 1, 0	D/A interface. Word clock f=2Fs.
20	LRCK	O 1, 0	D/A interface. LR clock f=F _s .
21	PCMD	O 1, 0	D/A interface. Serial data (two's complement, MSB first).
22	BCK	O 1, 0	D/A interface. Bit clock.
23	GTOP	O 1, 0	GTOP output.
24	XUGF	O 1, 0	XUGF output.
25	XPCK	O 1, 0	XPLCK output.
26	Vdd	- -	Power supply (+5V).
27	GFS	O 1, 0	GFS output.
28	RFCK	O 1, 0	RFCK output.
29	C2PO	O 1, 0	C2PO output.
30	XROF	O 1, 0	XRAOF output.
31 ~ 33	MNT3, 1, 0	O 1, 0	MNT 3, MNT 1, MNT 0 output.
34	XTAI	I	16.9344MHz crystal oscillation circuit input, or 33.8688MHz input.
35	XTAO	O 1, 0	16.9344MHz crystal oscillation circuit output.
36	XTSL	I	Crystal selection input. Set low when the crystal is 16.9344MHz, high when 33.8688MHz.
37	FSTT	O 1, 0	2/3 frequency divider output for pins 34 and 35.
38	C4M	O 1, 0	4.2336MHz output.
39	DOUT	O 1, 0	Digital-out output.
40	EMPH	O 1, 0	Outputs high signal when the playback disc has emphasis, low signal when no emphasis.
41	WFCK	O 1, 0	WFCK output.
42	Vss	- -	GND.
43	SCOR	O 1, 0	Outputs high signal when either sub code sync S0 or S1 is detected.
44	SBSO	O 1, 0	Sub P to W serial output.
45	EXCK	I	SBSO read-out clock input.
46	SQSO	O 1, 0	Sub Q 80-bit serial output.
47	SQCK	I	SQSO read-out clock input.
48	MUTE	I	High : mute, low : release
49	SENS	O 1, 0	SENS output to CPU.
50	XRST	I	System reset. Reset when low.
51	DATA	I	Serial data input from CPU.
52	XLAT	I	Latch input from CPU. Serial data is latched at the falling edge.
53	CLOK	I	Serial data transfer clock input from CPU.
54	SEIN	I	Sense input from SSP.
55	CNIN	I	Track jump count signal input.
56	DATO	O 1, 0	Serial data output to SSP.
57	XLTO	O 1, 0	Serial data latch output to SSP. Latched at the falling edge.
58	Vdd	- -	Power supply (+5V).
59	CLKO	O 1, 0	Serial data transfer clock output to SSP.
60 ~ 63	SPOA ~ SPOD	I	μ-com extended interface (input A ~ D).
64	XLON	O 1, 0	μ-com extended interface (output).

Notes • PCMD is two's complement output of MSB first.

- GTOP is used to monitor the frame sync protection status.
- XUGF is the negative pulse for the frame sync derived from the EFM signal. It is the signal before sync protection.
- XPLCK is the inverse of EFM PLL clock. The PLL is designed so that the falling edge and the EFM signal transition point coincide.
- GFS goes high when the frame sync and the insertion protection timing match.
- RFCK is derived from the crystal accuracy. This signal has cycle of 136μ.
- C2PO represents the data error status.
- XRAOF is generated when the 16K RAM exceeds the ±4F jitter margin.

ADJUSTMENT

ADJUSTMENT

No.	ITEM	INPUT SETTING	OUTPUT SETTING	PLAYER SETTING	ALIGNMENT POINT	ALIGN FOR	FIG.
1	TRACKING ERROR BALANCE	Test disc Type 4	Connect an oscilloscope as follows. CH1: RF (CN3-6) CH2: TE (CN3-1)	Press the P. OPEN/CLOSE key to open the tray. Reset to TEST mode. Then, press the CHECK key. Confirm that the display is "03".	TE BALANCE VR2	Symmetry between upper and lower patterns, or DC=0±0.05V	(a)
2	FOCUS ERROR BALANCE	Test disc Type 4	Connect an oscilloscope as follows. CH1: RF (CN3-6) CH2: TE (CN3-1)	Press the PLAY key. Confirm that the display is "05".	FE BALANCE VR1	Optimum eyepattern Grating is correctly aligned with the RF level of 1.5Vp-p or more and the TE (servo open) level of 1.5Vp-p or more. the pickup is acceptable.	(b)
3	FOCUS GAIN	Test disc Type 4 Apply signal of 1kHz, 0.1Vrms to CN3 pin 4 and 5.	Connect a LPF to CN3 pin 4-5, to which connect an oscilloscope or two AC voltmeters.	Press the PLAY key. Confirm that the display is "05".	FOCUS GAIN VR3	Two VTVMs should read the same value.	(c)
4	TRACKING GAIN	Test disc Type 4 Apply signal of 1.3kHz, 0.1Vrms to CN3 pin 1 and 2.	Connect a LPF to CN3 pin 1-2, to which connect an oscilloscope or two AC voltmeters.	Press the PLAY key. Confirm that the display is "05".	TRACKING GAIN VR4	Two VTVMs should read the same value.	(c)

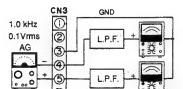
(NOTE) Type 4 disc : SONY YEDS-18 TEST DISC or equivalent.

LPP: around 47kohms×390pF or so.

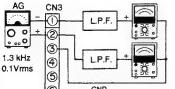
Adjustment procedures are in TEST MODE.

(c)Focus Gain and Tracking Gain Adj.

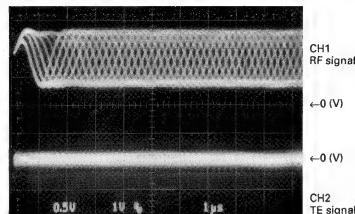
Focus gain adj.



Tracking gain adj.

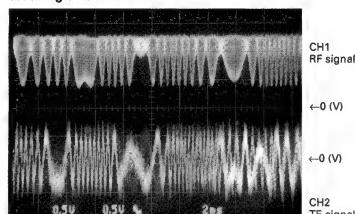


RF level Wave-form



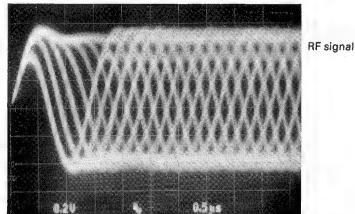
- RF signal and E.Spot signal in test mode (PLAY).

Tracking error balance



- RF signal and T.Error signal; in test mode (Focusing ON).
- Adjust T.Error so that the waveform is symmetrical above and below 0V (VR2).

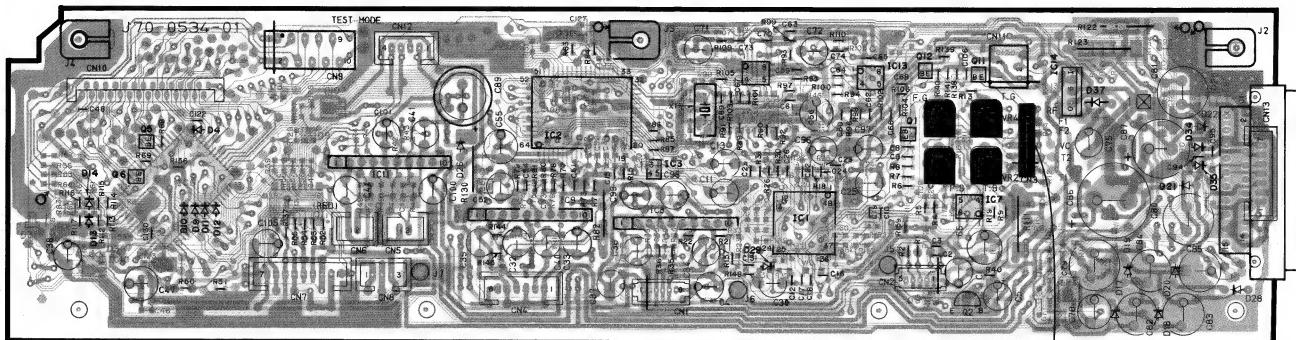
Focus error balance



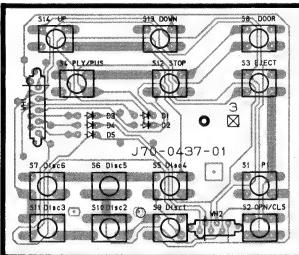
- RF signal in test mode (PLAY).
- Perform the tangential and focusing offset adjustments so that each of the center cross points are focused into one point on the display. The crossing points above and below the center shall also be displayed clearly.

PC BOARD (COMPONENT SIDE VIEW)

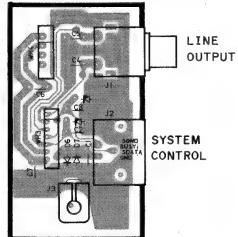
X32-2670-71



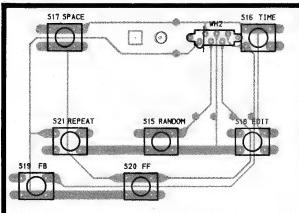
X25-5350-70 A/3



X25- C/3



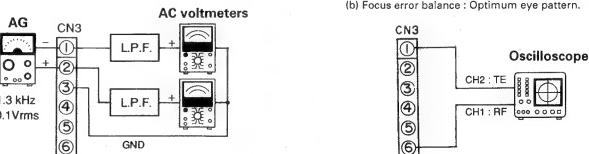
X25-B/3



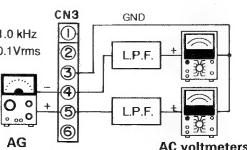
(c) Tracking gain : Two VTVMs should read the same value.

(a) Tracking error balance : Symmetry between upper and lower patterns or $DC=0\pm 0.05V$,

(b) Focus error balance : Optimum eye pattern.



(c) Focus gain : Two VTVMs should read the same value.



DP-M87

PARTS LIST

CAPACITORS

CC	45	TH	1H	220	J
1	2	3	4	5	6

1 = Type ... ceramic, electrolytic, etc.
2 = Shape ... round, square, ect.
3 = Temp. coefficient
4 = Voltage rating
5 = Value
6 = Tolerance



Color*

010 = 1pF
100 = 10pF
101 = 100pF
102 = 1000pF = 0.001μF
103 = 0.01μF

Capacitor value

2	2	0 = 22pF
		Multiplier 2nd number

Example : CC45TH = 470 ± 60ppm/C

Temperature coefficient

1st Word	C	L	P	R	S	T.	U
Color*	Black	Red	Orange	Yellow	Green	Blue	Violet

pppm/C °C
0 -80 -150 -220 -330 -470 -750

2nd Word	G	H	J	K	L
pppm/C	+30	+60	+120	+250	±500

Example : CC45TH = 470 ± 60ppm/C

Tolerance (More than 10pF)

Code	C	D	G	J	K	M	X	Z	P	No code
(%)	±0.25	±0.5	±2	±5	±10	±20	+40	+80	+100	More than 10μF -10 ~ +50

Less than 10pF										
Code	B	C	D	F	G					

(pF) ±0.1 ±0.25 ±0.5 ±1 ±2

Voltage rating

2nd word	A	B	C	D	E	F	G	H	J	K	V
1st word	0	1.0	1.25	1.6	2.0	2.5	3.15	4.0	5.0	6.3	8.0
0	10	12.5	16	20	25	31.5	40	50	63	80	35
1	100	125	160	200	250	315	400	500	630	800	-
2	1000	1250	1600	2000	2500	3150	4000	5000	6300	8000	-
3	10000	12500	16000	20000	25000	31500	40000	50000	63000	80000	-

Chip capacitors

(EX)	C	C	7	3	F	S	L	1	H	0	0	J
	1	2	3	4	5	6	7					

Dimension (Chip capacitors)

Dimension code	L	W	T
Empty	5.6 ± 0.5	5.0 ± 0.5	Less than 2.0
A	4.5 ± 0.5	3.2 ± 0.4	Less than 2.0
B	4.5 ± 0.5	2.0 ± 0.3	Less than 2.0
C	4.5 ± 0.5	1.25 ± 0.2	Less than 1.25
D	3.2 ± 0.4	2.5 ± 0.3	Less than 1.5
E	3.2 ± 0.2	1.6 ± 0.2	Less than 1.25
F	2.0 ± 0.3	1.25 ± 0.2	Less than 1.25
G	1.6 ± 0.2	0.8 ± 0.2	Less than 1.0

Refer to the table above.

1 = Type

2 = Shape

3 = Dimension

4 = Temp. coefficient

5 = Voltage rating

6 = Value

7 = Tolerance

RESISTORS

Chip resistor (Carbon)

(EX)	R	K	7	3	E	B	2	B	0	0	J
	1	2	3	4	5	6	7				

Dimension



Dimension code	L	W	T
E	3.2 ± 0.2	1.6 ± 0.2	1.0
F	2.0 ± 0.3	1.25 ± 0.2	1.0
G	1.6 ± 0.2	0.8 ± 0.2	0.5 ± 0.1

Carbon resistor (Normal type)

(EX)	R	D	1	4	B	B	2	C	0	0	J
	1	2	3	4	5	6	7				

Dimension (Chip resistor)

Dimension code	L	W	T
1J	1/16W	2C	1/6W
2A	1/10W	2E	1/4W
2B	1/8W	2H	1/2W

1 = Type

5 = Rating wattage

2 = Shape

6 = Value

3 = Dimension

7 = Tolerance

4 = Temp. coefficient

DP-M87

SPECIFICATIONS

Format

System Compact disc digital audio system

Laser Semiconductor laser

Number of channels 2 channels

Playing rotation 200rpm ~ 500rpm (CLV)

Dynamic range More than 90dB

Total harmonic distortion Less than 0.005%

Channel separation More than 90dB

Wow & flutter Unmeasurable limit

Output level/impedance 2.2V / 1kΩ

D/A Convertors

D/A conversion 1 Bit

Oversampling 8fs (352.8kHz)

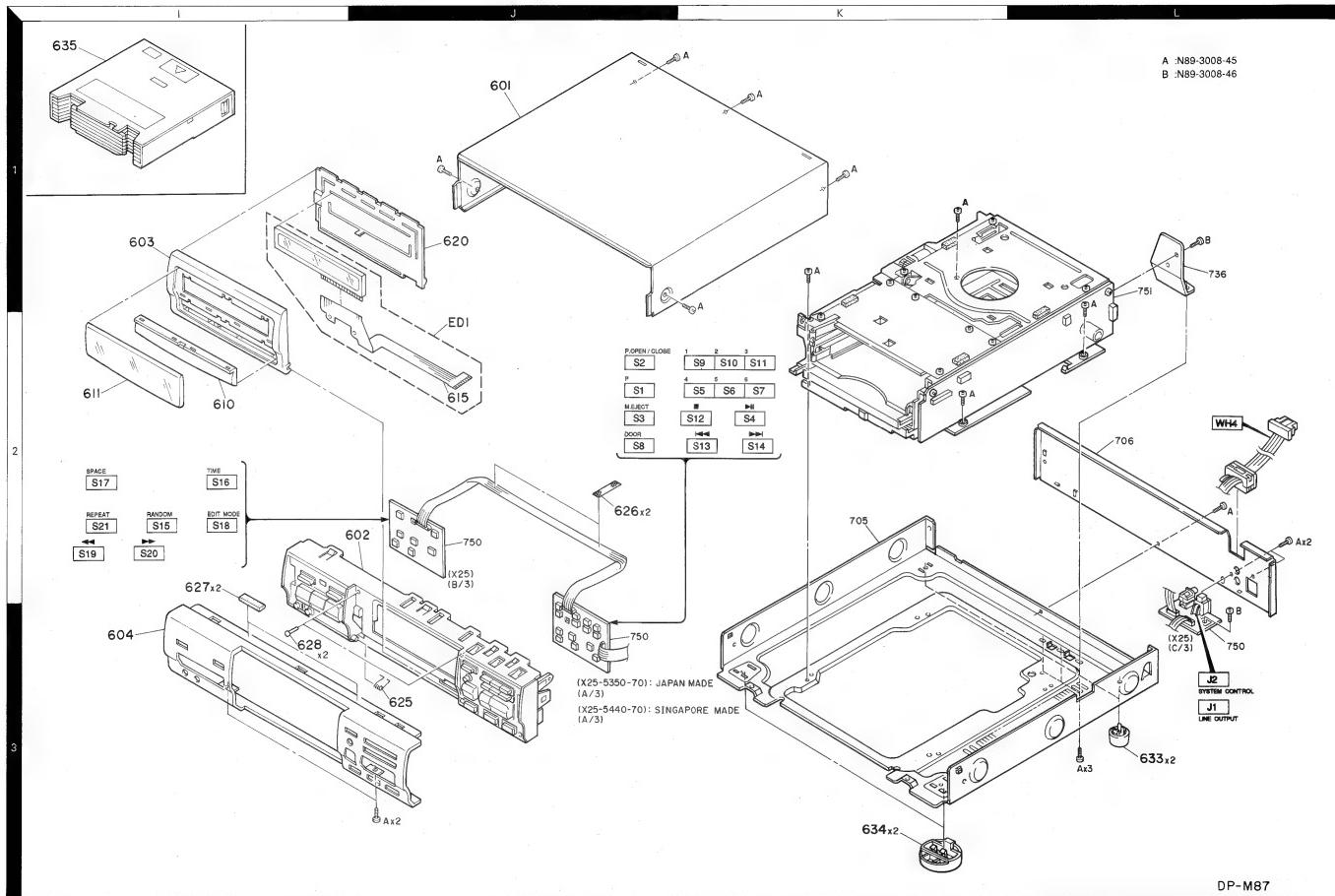
Audio

Frequency response 20Hz ~ 20kHz, ±1.0dB

Signal to noise ratio More than 96dB

Note : KENWOOD follows a policy of continuous advancements in development. For this reason specifications may be changed without notice.

**DP-M87 DP-M87
EXPLODED VIEW (UNIT)**



DP-M87

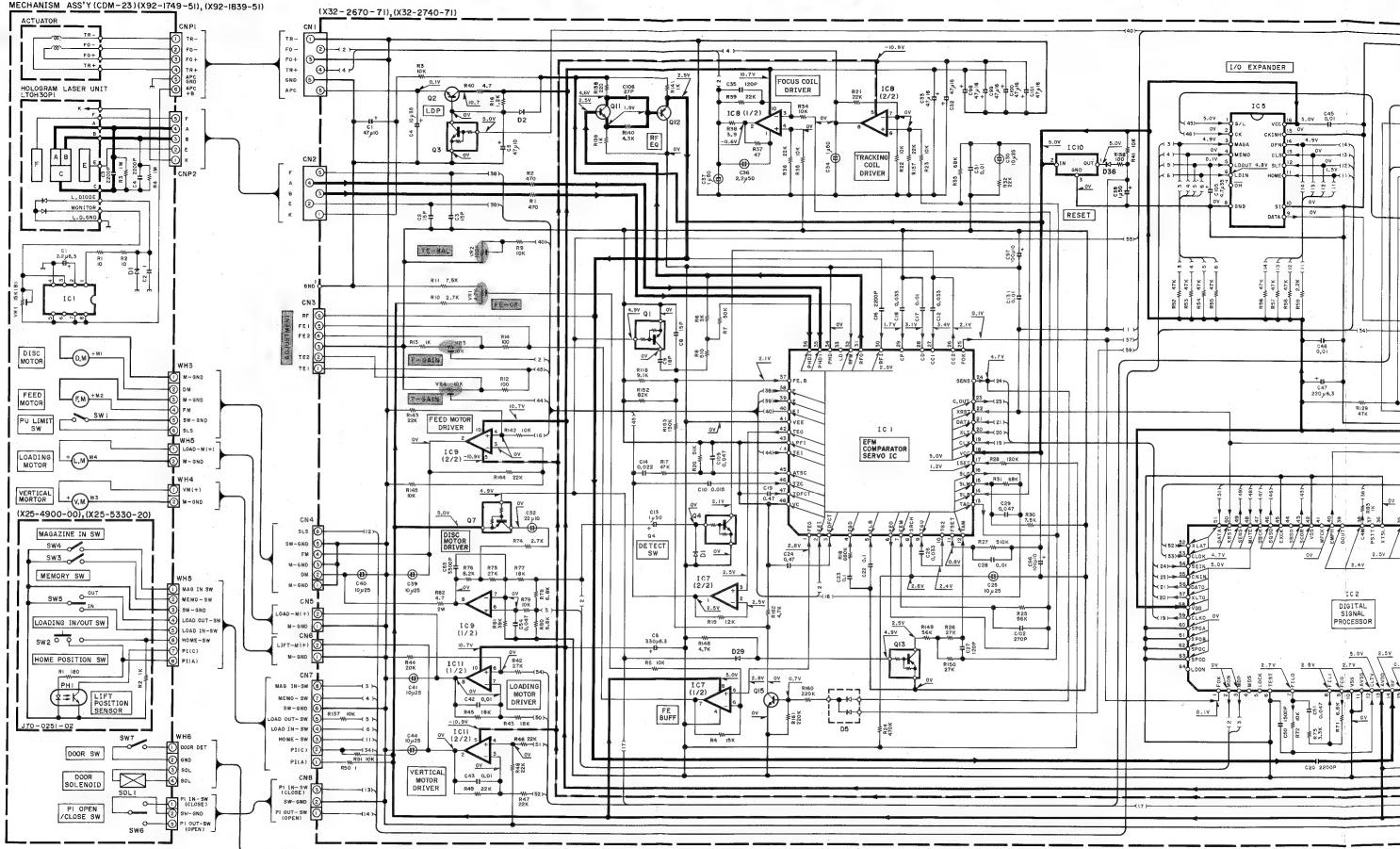
Parts with the exploded numbers larger than 700 are not supplied.

PARTS LIST

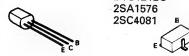
Y : AAFFES (Europe) X : Australia M : Other Areas

DP-M87

PARTS LIST



2SC324



DTA124EU
DTC124EU
2SA1576
2SC4081

2SB1308



2SD1963



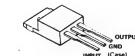
LA6510
TA8410AK



N1M4E66M



17

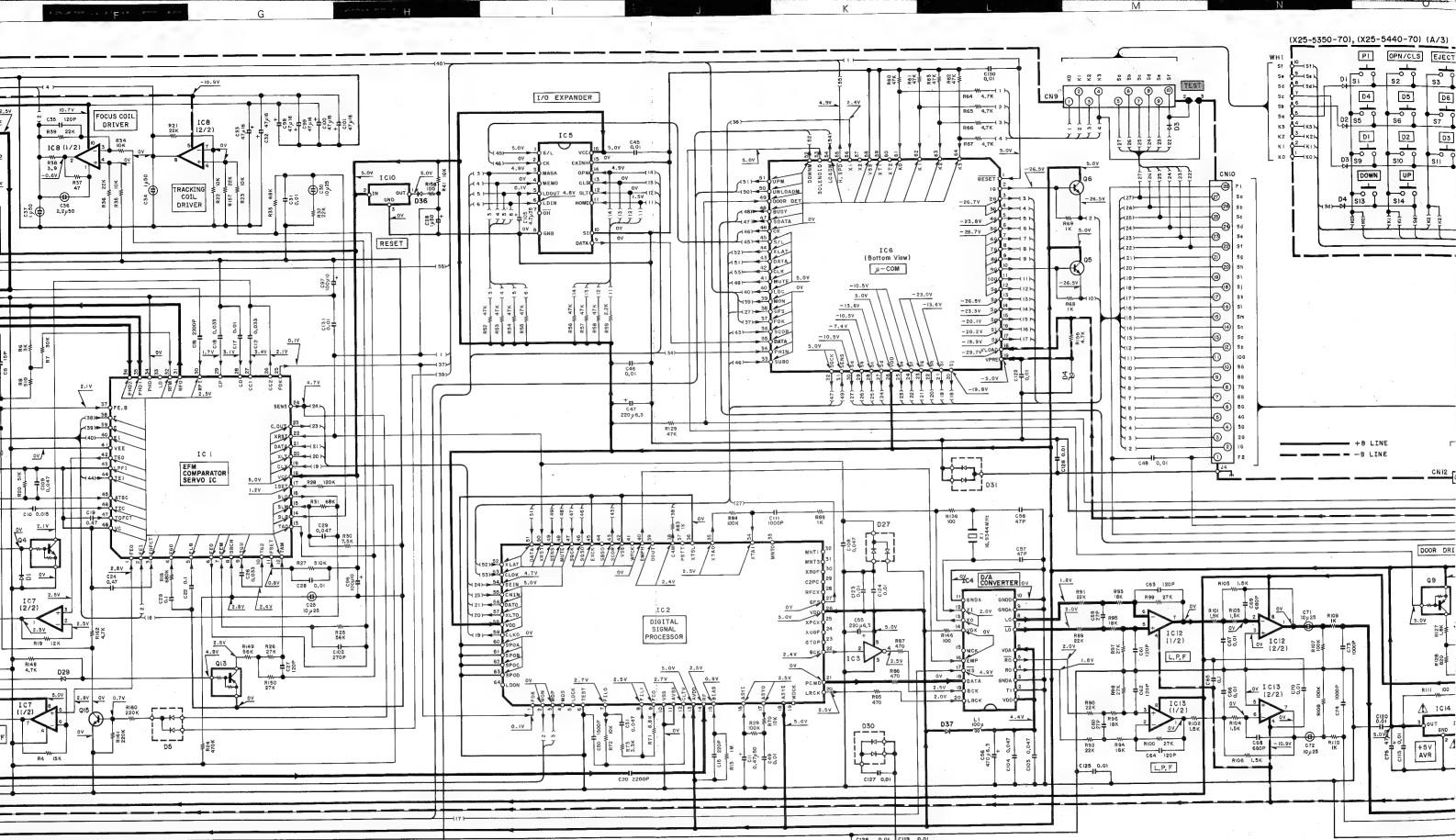


CXD2517Q



CXA1782





G

H

I

J

K

L

M

N

TC74HC165AF

LM2940CT-5.0

CXD2517Q

SC75217GF-642

CXA1782BQ

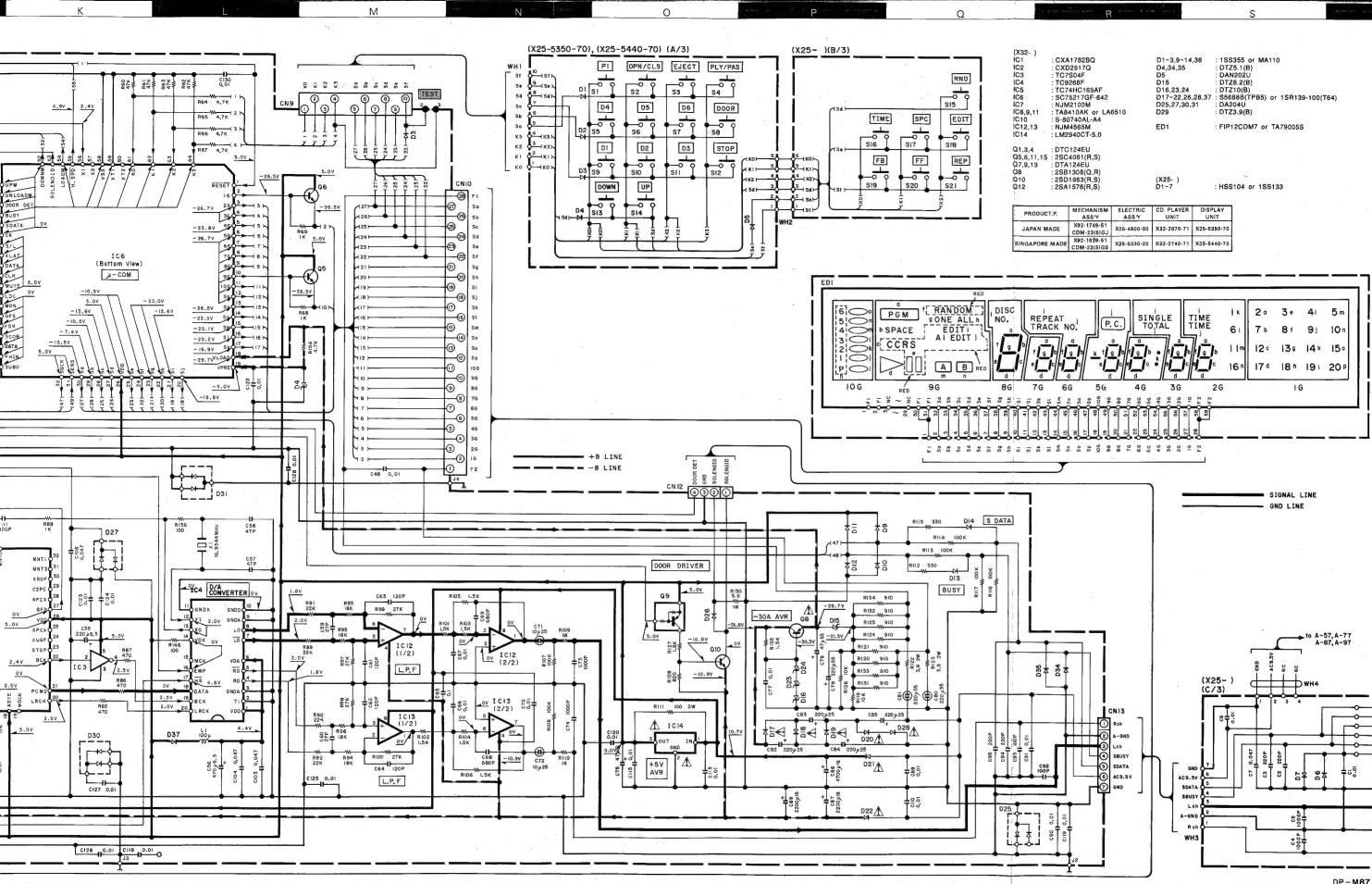
TC9268F

S-80740AL-A4

TC7S04F

• DC voltages are as measured with a high impedance voltmeter/and units.

CAUTION : For continued safety, replace safety critical components. Δ Indicates safety critical components. To reduce the risk of fire, do not expose parts to flame or heat. Exposed parts are acceptably insulated from the supply circuit.



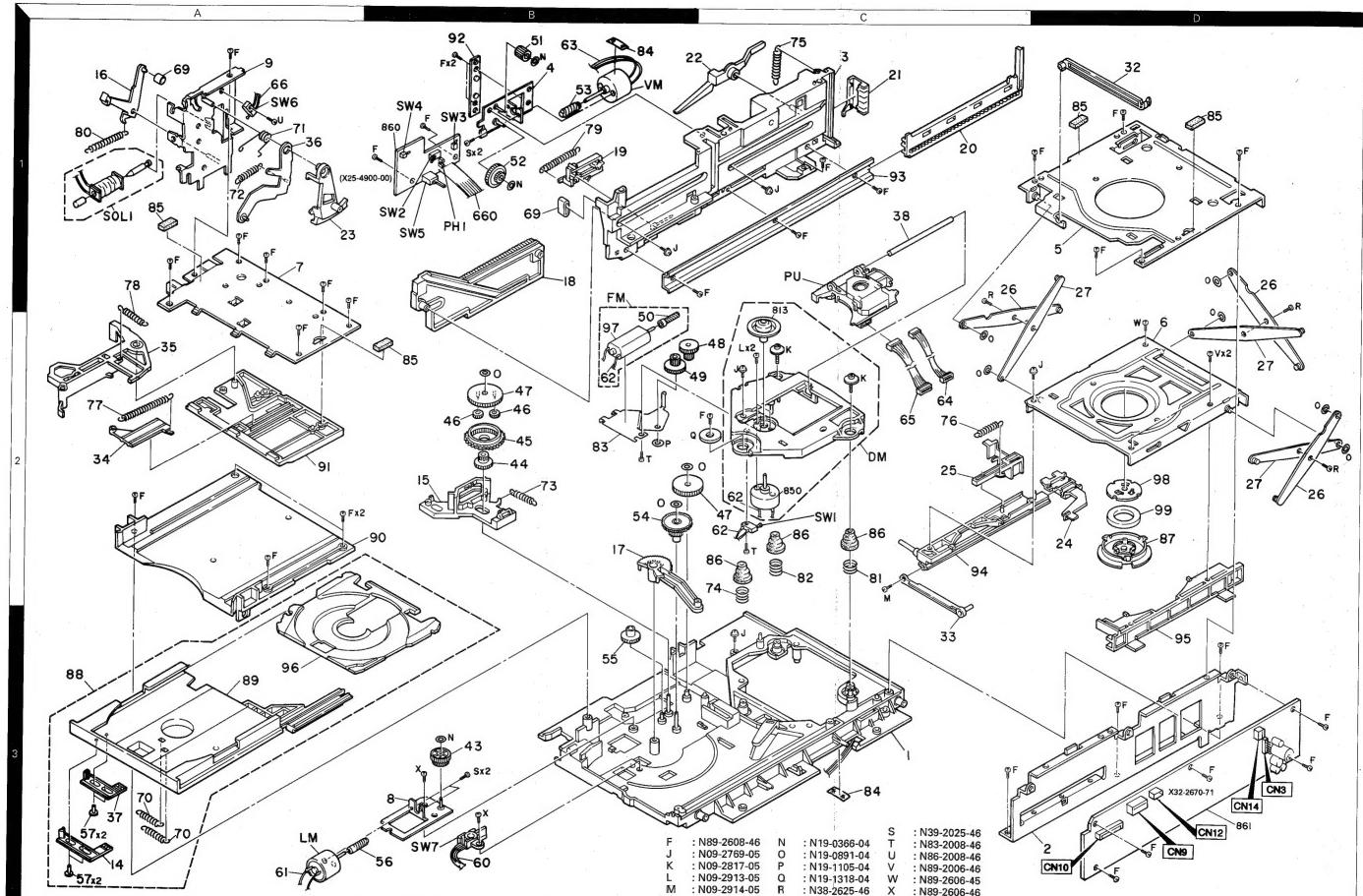
- DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

CAUTION : For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). ¹ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

This schematic diagram is available of changing information from serial No. 31240001.

Y22-3360-21

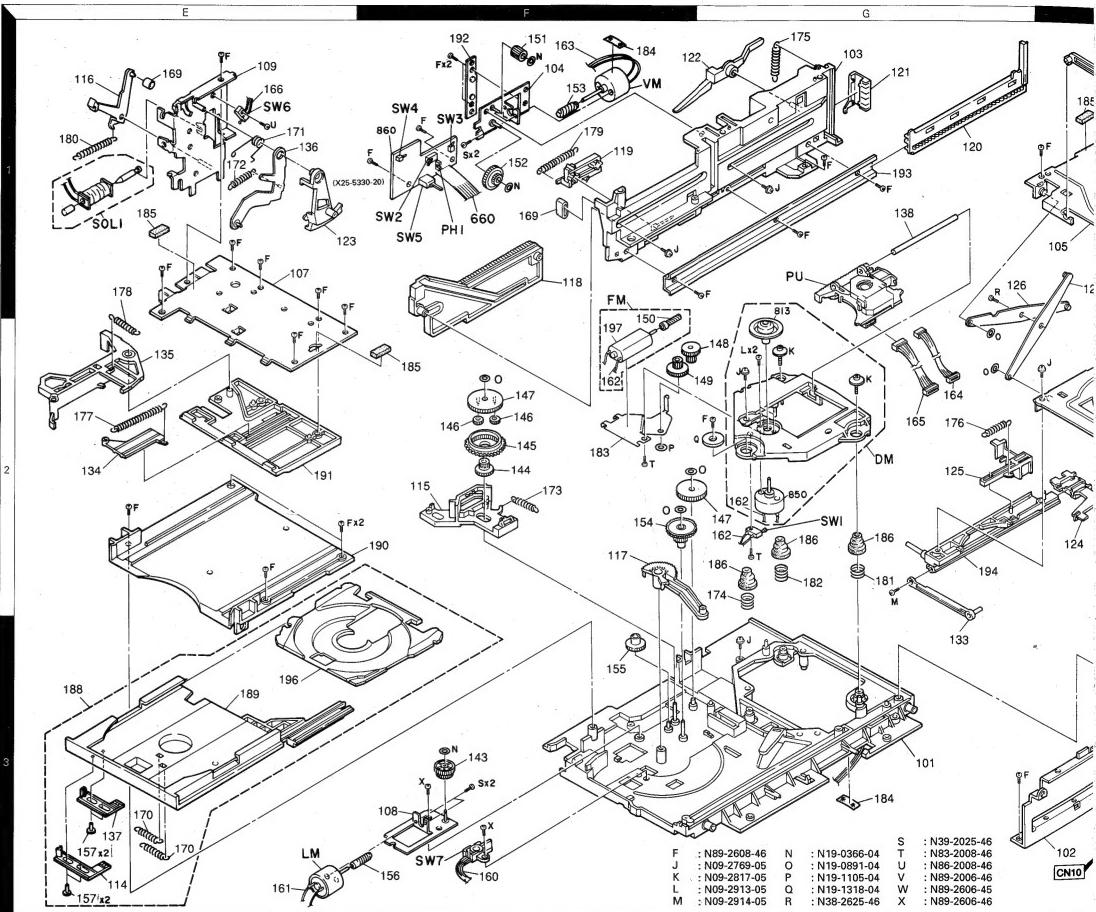
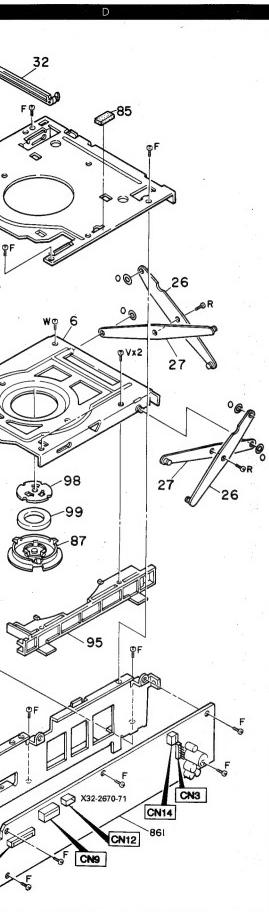
DP-M87
KENWOOD



F : N89-2608-46	N : N19-0366-04	S : N35-
J : N09-2769-05	O : N19-0891-04	T : N83-
K : N09-2817-05	P : N19-1105-04	U : N86-
L : N09-2913-05	Q : N19-1318-04	V : N89-
M : N09-2914-05	R : N38-2625-46	W : N89-

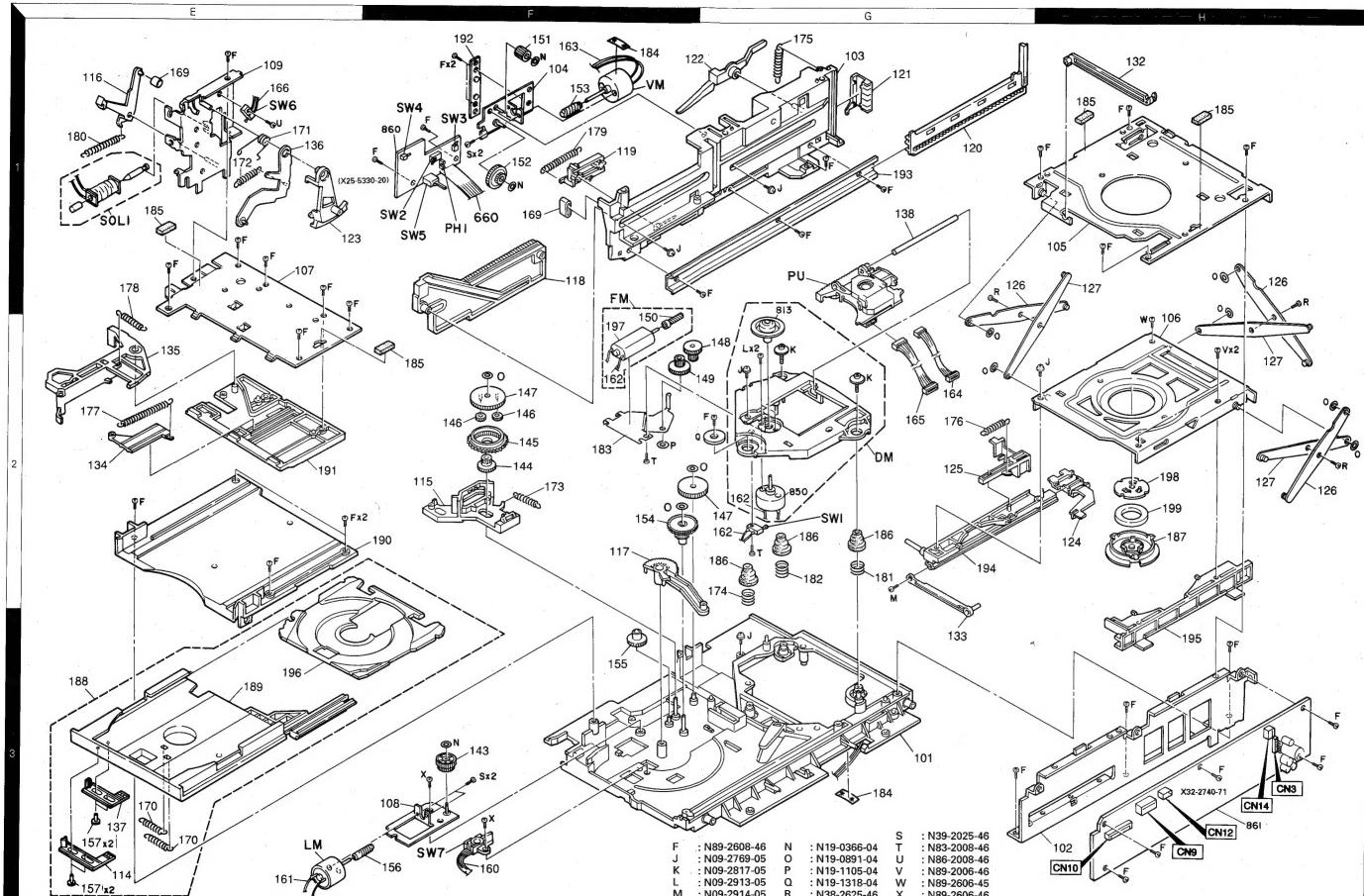
The diagram illustrates a connector assembly. It features a central vertical pin labeled '6' and two side pins labeled '2' and '10'. A horizontal bar labeled 'CN10' is positioned above the central pin, and another labeled 'CN9' is below it. To the right, a component labeled 'CN12' is shown. Three force vectors labeled 'F' are applied to the pins: one pointing upwards from pin 2, one pointing downwards from pin 10, and one pointing downwards from pin 6.

DP-M87 DP-M87
EXPLODED VIEW (MECHANISM) : SINGAPORE MADE



I numbers larger than 700 are not supplied.

DP-M87 **DP-M87**
EXPLODED VIEW (MECHANISM) : SINGAPORE MADE



F : N89-2608-46	N : N19-0366-04	S : N39-
J : N09-2769-05	O : N19-0891-04	T : N83-
K : N09-2817-05	P : N19-1105-04	U : N86-
L : N09-2913-05	Q : N19-1318-04	V : N89-
M : N09-2914-05	R : N19-2625-46	W : N89-

Parts with the exploded numbers larger than 700 are not supplied.